Parallel Processing: Performance Limits & Synchronization

Daniel Sanchez
Computer Science & Artificial Intelligence Lab
M.I.T.
Administrivia

• Quiz 3: Thursday 7:30-9:30pm, room 50-340 (Walker)
  – Closed book, covers lectures 15-21
Administrivia

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- Design project:
  - Part 1 (15 points) is mandatory, part 2 (15 points) is optional
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  - If you are taking the makeup or have missing labs, we will project your grade on those
  - You can then decide how much effort to put on part 2 of the design project based on where you stand
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Please evaluate 6.S084. Your feedback matters!
The Shift to Multicore

[Produced with CPUDB, cpudb.stanford.edu]
The Shift to Multicore

- Since 2005, improvements in system performance mainly due to increasing cores per chip

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- Since 2005, improvements in system performance mainly due to increasing cores per chip
- Why?

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The Shift to Multicore

- Since 2005, improvements in system performance mainly due to increasing cores per chip
- Why? Limited instruction-level parallelism
Multicore Processors

- If applications have a lot of parallelism, using a larger number of simpler cores is more efficient!
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Cost/perf curve of possible core designs

- High-perf, expensive core
- Moderate perf, efficient core

Cost (area, energy...)

Performance

2 cores

4 cores
Multicore Processors

- If applications have a lot of parallelism, using a larger number of simpler cores is more efficient!

What is the optimal tradeoff between core cost and number of cores?
Amdahl’s Law

- Speedup = \( \frac{\text{time}_{\text{without enhancement}}}{\text{time}_{\text{with enhancement}}} \)
- Suppose an enhancement speeds up a fraction \( f \) of a task by a factor of \( S \)

\[
\text{time}_{\text{new}} = \text{time}_{\text{old}} \cdot \left( (1-f) + \frac{f}{S} \right)
\]

\[
S_{\text{overall}} = \frac{1}{(1-f) + \frac{f}{S}}
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\]

Corollary: Make the common case fast
Amdahl’s Law and Parallelism

- Say you write a program that can do 90% of the work in parallel, but the other 10% is sequential
- What is the maximum speedup you can get by running on a multicore machine?
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\[
f = 0.9, \; S=\infty \rightarrow S_{\text{overall}} = 10
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What \( f \) do you need to use a 1000-core machine well?
Amdahl’s Law and Parallelism

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What \( f \) do you need to use a 1000-core machine well?

\[ 1 / (1-f) = 1000 \rightarrow f = 0.999 \]
Thread-Level Parallelism

- Divide computation among multiple threads of execution
  - Each thread executes a different instruction stream
Thread-Level Parallelism

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- Communication models:
  - Shared memory:
    - Single address space
    - Implicit communication by memory loads & stores
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  – Message passing:
    • Separate address spaces
    • Explicit communication by sending and receiving messages
Thread-Level Parallelism

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• Communication models:
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  – Message passing:
    • Separate address spaces
    • Explicit communication by sending and receiving messages
  – Pros/cons of each model?
Synchronous Communication

**PRODUCER**

```
loop:<xxx>;
send(c);
goto loop
```

- `<xxx_1>`
- `<xxx_2>`
- `<xxx_3>`

**CONSUMER**

```
loop:c = rcv();
<yyy>;
goto loop
```

- `<yyy_1>`
- `<yyy_2>`
- `<yyy_3>`
Synchronous Communication

**PRODUCER**

- \(<xxx_1>\) \(\rightarrow\) send\(_1\)
- \(<xxx_2>\) \(\rightarrow\) send\(_2\)
- \(<xxx_3>\) \(\rightarrow\) send\(_3\)

**CONSUMER**

- \(<yyy_1>\) \(\rightarrow\) rcv\(_1\)
- \(<yyy_2>\) \(\rightarrow\) rcv\(_2\)
- \(<yyy_3>\) \(\rightarrow\) rcv\(_3\)

**Precedence Constraints:**

- \(a \preceq b\)

- “\(a \) precedes \(b\)”
Synchronous Communication

Precedence Constraints:

- Can’t consume data before it’s produced
  
\[ \text{send}_i \leq \text{rcv}_i \]
Synchronous Communication

Precedence Constraints:

\[ a \preceq b \]

“\( a \) precedes \( b \)"

- Can’t consume data before it’s produced
  \[ \text{send}_i \preceq \text{rcv}_i \]
- Producer can’t “overwrite” data before it’s consumed
  \[ \text{rcv}_i \preceq \text{send}_{i+1} \]
FIFO (First-In First-Out) Buffers

FIFO buffers relax synchronization constraints. The producer can run up to N values ahead of the consumer:

\[ r_{cv_i} \leq s_{end_{i+N}} \]
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Typically implemented as a “Ring Buffer” in shared memory:
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Typically implemented as a “Ring Buffer” in shared memory:

Read ptr

Write ptr

<xxx>; send(c₀);
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Typically implemented as a “Ring Buffer” in shared memory:

![Diagram of FIFO Buffers](image)
void send(char c){
    buf[in] = c;
    in = (in + 1) % N;
}

char rcv(){
    char c;
    c = buf[out];
    out = (out + 1) % N;
    return c;
}
** overset **

**SHARED MEMORY:**

```c
char buf[N];          /* The buffer */
int in=0, out=0;
```

**PRODUCER:**

```c
void send(char c){
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```

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char rcv(){
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```

Not correct. Why?
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}

char rcv(){
    char c;
    c = buf[out];
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    return c;
}

Not correct. Why?

Doesn’t enforce any precedence constraints (e.g., rcv() could be invoked prior to any send() )
Semaphores (Dijkstra, 1962)

Programming construct for synchronization:
- New data type: semaphore, an integer ≥ 0
  `semaphore s = K;  // initialize s to K`
Semaphores (Dijkstra, 1962)

Programming construct for synchronization:
- New data type: *semaphore*, an integer $\geq 0$
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- New operations (defined on semaphores):
  - `wait(semaphore s)`
    ```
    wait until s > 0, then s = s - 1
    ```
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  - `wait(semaphore s)`
    
    *wait until $s > 0$, then $s = s - 1$*
  
  - `signal(semaphore s)`
    
    *$s = s + 1$ (one waiting thread may now be able to proceed)*
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- Semantic guarantee: A semaphore \( s \) initialized to \( K \) enforces the precedence constraint:
  
  \[ \text{signal}(s)_i < \text{wait}(s)_{i+K} \]
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    `wait until s > 0, then s = s - 1`
  - `signal(semaphore s)`
    `s = s + 1 (one waiting thread may now be able to proceed)`
- Semantic guarantee: A semaphore $s$ initialized to $K$ enforces the precedence constraint:
  \[
  \text{signal}(s)_i < \text{wait}(s)_{i+K}
  \]
  
  The $i^{th}$ call to `signal(s)` must complete before the $(i+K)^{th}$ call to `wait(s)` completes
## Semaphores for Precedence

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Goal: Want statement A2 in thread A to complete before statement B4 in thread B begins.
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A2 < B4
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**Recipe:**

A2 < B4
### Semaphores for Precedence

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**Recipe:**
- Declare semaphore = 0

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**Semaphores for Precedence**

Goal: Want statement A2 in thread A to complete before statement B4 in thread B begins.

**A2 < B4**

Recipe:
- Declare semaphore = 0
- signal(s) at start of arrow

```c
semaphore s = 0;
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May 8, 2018

L22-12
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Goal: Want statement A2 in thread A to complete before statement B4 in thread B begins.

Recipe:
- Declare semaphore = 0
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- wait(s) at end of arrow

A2 < B4
Semaphores for Precedence

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```c
semaphore s = 0;

Thread A   Thread B
A1;        B1;
A2;        B2;
signal(s); B2;
A3;        B3;
wait(s);   B4;
A4;        B4;
A5;        B5;
```
Semaphores for Resource Allocation

Abstract problem:
• Pool of K resources
• Many threads, each needs resource for occasional uninterrupted period
• Must guarantee that at most K resources are in use at any time

Solution using semaphores:
In shared memory:

```c
semaphore s = K;  // K resources
```

Using resources:

```c
wait(s);  // Allocate a resource
...
    // use it for a while
signal(s);  // return it to pool
```

Invariant: Semaphore value = number of resources left in pool
FIFO Buffer with Semaphores: 2\textsuperscript{nd} Try

**SHARED MEMORY:**

```c
char buf[N];          /* The buffer */
int in = 0, out = 0;
semaphore chars = 0;
```

**PRODUCER:**

```c
void send(char c) {
    buf[in] = c;
    in = (in + 1) % N;
    signal(chars);
}
```

**CONSUMER:**

```c
char rcv() {
    char c;
    wait(chars);
    c = buf[out];
    out = (out + 1) % N;
    return c;
}
```
**FIFO Buffer with Semaphores: 2nd Try**

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```

Precedence managed by semaphore: \( \text{send}_i < \text{rcv}_j \)
Resource managed by semaphore: \# of chars in buf
FIFO Buffer with Semaphores: 2nd Try

**PRODUCER:**
void send(char c) {
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**CONSUMER:**
char rcv() {
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Precedence managed by semaphore: send_i < rcv_i
Resource managed by semaphore: # of chars in buf

Still not correct. Why?
FIFO Buffer with Semaphores: 2nd Try

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```c
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    char c;
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    return c;
}
```

Precedence managed by semaphore: send$_i$ $<$ rcv$_i$

Resource managed by semaphore: # of chars in buf

Still not correct. Why?

Producer can overflow buffer.
Must enforce rcv$_i$ $<$ send$_{i+N}$
FIFO Buffer with Semaphores
Correct implementation (for single producer + single consumer)

**SHARED MEMORY:**

```c
char buf[N]; // The buffer
int in = 0, out = 0;
semaphore chars = 0, spaces = N;
```

**PRODUCER:**

```c
void send(char c) {
    wait(spaces);
    buf[in] = c;
    in = (in + 1) % N;
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char rcv() {
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}
```

Resources managed by semaphores: characters in FIFO, spaces in FIFO.
Resources managed by semaphores: characters in FIFO, spaces in FIFO.

Works with single producer and consumer. But what about multiple producers and consumers?
Suppose you and your friend visit the ATM at exactly the same time, and remove $50 from your account. What happens?

```java
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
}
```

What is *supposed* to happen?
Simultaneous Transactions

Suppose you and your friend visit the ATM at exactly the same time, and remove $50 from your account. What happens?

```c
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
}
```

What is *supposed* to happen?

// assume t0 has address of balance[account]

<table>
<thead>
<tr>
<th>Thread #1</th>
<th>Thread #2</th>
</tr>
</thead>
<tbody>
<tr>
<td>lw t1, 0(t0)</td>
<td>lw t1, 0(t0)</td>
</tr>
<tr>
<td>sub t1, t1, a1</td>
<td>sub t1, t1, a1</td>
</tr>
<tr>
<td>sw t1, 0(t0)</td>
<td>sw t1, 0(t0)</td>
</tr>
<tr>
<td>...</td>
<td>...</td>
</tr>
</tbody>
</table>

Result: You have $100, and your bank balance is $100 less.
But What If Both Calls Interleave?

// assume t0 has address of balance[account]

Thread #1

lw t1, 0(t0)
sub t1, t1, a1
sw t1, 0(t0)
...

Thread #2

lw t1, 0(t0)
sub t1, t1, a1
sw t1, 0(t0)
...

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But What If Both Calls Interleave?

// assume t0 has address of balance[account]

Thread # 1                      Thread # 2

lw t1, 0(t0)                     lw t1, 0(t0)
                                 sub t1, t1, a1
                                 sw t1, 0(t0)

sub t1, t1, a1
sw t1, 0(t0)
...
...

Result: You have $100, and your
         bank balance is only $50 less!
But What If Both Calls Interleave?

We need to be careful when writing concurrent programs. In particular, when modifying shared data.

// assume t0 has address of balance[account]

Thread # 1
lw t1, 0(t0)
sub t1, t1, a1
sw t1, 0(t0)

Thread #2
lw t1, 0(t0)
sub t1, t1, a1
sw t1, 0(t0)

Result: You have $100, and your bank balance is only $50 less!
But What If Both Calls Interleave?

// assume t0 has address of balance[account]

Thread # 1                        Thread #2
lw t1, 0(t0)                      lw t1, 0(t0)
sub t1, t1, a1                    sub t1, t1, a1
sw t1, 0(t0)                      sw t1, 0(t0)
...                                ...

sub t1, t1, a1
sw t1, 0(t0)
...                                ...

Result: You have $100, and your bank balance is only $50 less!

We need to be careful when writing concurrent programs. In particular, when modifying shared data.

For certain code segments, called critical sections, we would like to ensure that no two executions overlap.
But What If Both Calls Interleave?

We need to be careful when writing concurrent programs. In particular, when modifying shared data.

For certain code segments, called critical sections, we would like to ensure that no two executions overlap.

This constraint is called mutual exclusion.

// assume t0 has address of balance[account]

Thread # 1                      Thread # 2
lw t1, 0(t0)                    lw t1, 0(t0)
                             sub t1, t1, a1
                             sw t1, 0(t0)
sub t1, t1, a1
sw t1, 0(t0)
...
...

Result: You have $100, and your bank balance is only $50 less!
But What If Both Calls Interleave?

We need to be careful when writing concurrent programs. In particular, when modifying shared data.

For certain code segments, called **critical sections**, we would like to ensure that no two executions overlap.

This constraint is called **mutual exclusion**.

Solution: embed critical sections in wrappers (e.g., "transactions") that guarantee their atomicity, i.e., make them appear to be single, instantaneous operations.

```c
// assume t0 has address of balance[account]

Thread # 1                          Thread # 2
lw t1, @t0
lw t1, @t0
sub t1, t1, a1
sub t1, t1, a1
sw t1, @t0
sw t1, @t0

...  ...

Result: You have $100, and your bank balance is only $50 less!
```
Semaphores for Mutual Exclusion

```c
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
}
```
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
}

a <=> b
“a precedes b
or
b precedes a”
(i.e., they don’t overlap)
Semaphores for Mutual Exclusion

semaphore lock = 1;

void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
}

Lock controls access to critical section
semaphore lock = 1;

void debit(int account, int amount) {
    wait(lock); // Wait for exclusive access
    t = balance[account];
    balance[account] = t - amount;
}

Lock controls access to critical section

“a precedes b
or
b precedes a”
(i.e., they don’t overlap)
Semaphores for Mutual Exclusion

```c
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
    signal(lock); // Finished with lock
}
```

Lock controls access to critical section

Semaphore lock = 1;

“a precedes b
or
b precedes a”
(i.e., they don’t overlap)
void debit(int account, int amount) {
    wait(lock); // Wait for exclusive access
    t = balance[account];
    balance[account] = t - amount;
    signal(lock); // Finished with lock
}

Lock controls access to critical section

Issue: Lock granularity
    One lock for all accounts?
    One lock per account?
    One lock for all accounts ending in 004?

Semaphore lock = 1;

“a precedes b or b precedes a” (i.e., they don’t overlap)
void debit(int account, int amount) {
    t = balance[account];
    balance[account] = t - amount;
    signal(lock); // Finished with lock
}

Lock controls access to critical section

Issue: Lock granularity
    One lock for all accounts?
    One lock per account?
    One lock for all accounts ending in 004?

Locks are just one way to implement atomic transactions

Databases → Pessimistic vs optimistic concurrency control
Multicores → Software and hardware transactional memory
Hardware → Bluespec rules
Consider multiple producer threads:

```
... buf[in] = c;
in = (in+1) % N;
... ...
```

```
... buf[in] = c;
in = (in+1) % N;
... ...
```
Consider multiple producer threads:

```
buf[in] = c;
in = (in+1) % N;
...          ...

buf[in] = c;
in = (in+1) % N;
...          ...
```

Producer/Consumer Atomicity Problems

May 8, 2018
Consider multiple producer threads:

\[ \text{buf}[\text{in}] = c; \quad \text{in} = (\text{in}+1) \mod N; \]

\[ \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \quad \Quad
FIFO Buffer with Semaphores
Correct multi-producer, multi-consumer implementation

**SHARED MEMORY:**
```c
char buf[N];          /* The buffer */
int in = 0, out = 0;
semaphore chars = 0, spaces = N;
semaphore lock = 1;
```

**PRODUCER:**
```c
void send(char c) {
    wait(spaces);
    wait(lock);
    buf[in] = c;
    in = (in + 1) % N;
    signal(lock);
    signal(chars);
}
```

**CONSUMER:**
```c
char rcv() {
    char c;
    wait(chars);
    wait(lock);
    c = buf[out];
    out = (out + 1) % N;
    signal(lock);
    signal(spaces);
    return c;
}
```
A single synchronization primitive that enforces both:

**SHARED MEMORY:**

```c
char buf[N];          /* The buffer */
int in = 0, out = 0;
semaphore chars = 0, spaces = N;
semaphore lock = 1;
```

**PRODUCER:**

```c
void send(char c) {
    wait(spaces);
    wait(lock);
    buf[in] = c;
    in = (in + 1) % N;
    signal(lock);
    signal(chars);
}
```

**CONSUMER:**

```c
char rcv() {
    char c;
    wait(chars);
    wait(lock);
    c = buf[out];
    out = (out + 1) % N;
    signal(lock);
    signal(spaces);
    return c;
}
```
The Power of Semaphores

**SHARED MEMORY:**

```c
char buf[N];          /* The buffer */
int in = 0, out = 0;
semaphore chars = 0, spaces = N;
semaphore lock = 1;
```

**PRODUCER:**

```c
void send(char c) {
    wait(spaces);
    wait(lock);
    buf[in] = c;
    in = (in + 1) % N;
    signal(lock);
    signal(chars);
}
```

**CONSUMER:**

```c
char rcv() {
    char c;
    wait(chars);
    wait(lock);
    c = buf[out];
    out = (out + 1) % N;
    signal(lock);
    signal(spaces);
    return c;
}
```

A single synchronization primitive that enforces both:

Precedence relationships:

\[ send_i < rcv_i \]

\[ rcv_i < send_{i+N} \]
### The Power of Semaphores

**Shared Memory:**

```c
char buf[N];          /* The buffer */
int in = 0, out = 0;
semaphore chars = 0, spaces = N;
semaphore lock = 1;
```

**Producer:**

```c
void send(char c) {
    wait(spaces);
    wait(lock);
    buf[in] = c;
    in = (in + 1) % N;
    signal(lock);
    signal(chars);
}
```

**Consumer:**

```c
char rcv() {
    char c;
    wait(chars);
    wait(lock);
    c = buf[out];
    out = (out + 1) % N;
    signal(lock);
    signal(spaces);
    return c;
}
```

A single synchronization primitive that enforces both:

- **Precedence relationships:**
  - $send_i < rcv_i$
  - $rcv_i < send_{i+N}$

- **Mutual-exclusion relationships:**
  - Protect variables $in$ and $out$
Semaphores are themselves shared data and implementing wait and signal operations require read/modify/write sequences that must be executed as critical sections. So how do we guarantee mutual exclusion in these particular critical sections without using semaphores?
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Approaches:
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Approaches:

- Use a special instruction (e.g., “test and set”) that performs an atomic read-modify-write. Depends on atomicity of single instruction execution. This is the most common approach.
Semaphores are themselves shared data and implementing `wait` and `signal` operations require read/modify/write sequences that must be executed as critical sections. So how do we guarantee mutual exclusion in these particular critical sections without using semaphores?

Approaches:

- Use a special instruction (e.g., “test and set”) that performs an atomic read-modify-write. Depends on atomicity of single instruction execution. This is the most common approach.

- Implement them using system calls. Works in uniprocessors only, where the kernel is uninterruptible.
Semaphores are themselves shared data and implementing wait and signal operations require read/modify/write sequences that must be executed as critical sections. So how do we guarantee mutual exclusion in these particular critical sections without using semaphores?

Approaches:
- Use a special instruction (e.g., “test and set”) that performs an atomic read-modify-write. Depends on atomicity of single instruction execution. This is the most common approach.
- Implement them using system calls. Works in uniprocessors only, where the kernel is uninterruptible.
- Implement them using the atomicity of individual read and write operations, e.g., Dekker’s Algorithm (Wikipedia). Slow and rarely used.
The naïve use of synchronization constraints can introduce its own set of problems, particularly when a thread requires access to more than one protected resource.

```c
void transfer(int account1, int account2, int amount) {
    wait(lock[account1]);
    wait(lock[account2]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
    signal(lock[account2]);
    signal(lock[account1]);
}
```
The naïve use of synchronization constraints can introduce its own set of problems, particularly when a thread requires access to more than one protected resource.

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What can go wrong here?
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    signal(lock[account2]);
    signal(lock[account1]);
}
```

What can go wrong here?

Thread 1: wait(lock[6031]);
Thread 2: wait(lock[6004]);
Synchronization: The Dark Side

The naïve use of synchronization constraints can introduce its own set of problems, particularly when a thread requires access to more than one protected resource.

```c
void transfer(int account1, int account2, int amount) {
    wait(lock[account1]);
    wait(lock[account2]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
    signal(lock[account2]);
    signal(lock[account1]);
}
```

What can go wrong here?

Thread 1: wait(lock[6031]);
Thread 2: wait(lock[6004]);
Thread 1: wait(lock[6004]); // cannot complete
    // until thread 2 signals
Synchronization: The Dark Side

The naïve use of synchronization constraints can introduce its own set of problems, particularly when a thread requires access to more than one protected resource.

```c
void transfer(int account1, int account2, int amount) {
    wait(lock[account1]);
    wait(lock[account2]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
    signal(lock[account2]);
    signal(lock[account1]);
}
```

What can go wrong here?

Thread 1: wait(lock[6031]);
Thread 2: wait(lock[6004]);
Thread 1: wait(lock[6004]); // cannot complete
    // until thread 2 signals
Thread 2: wait(lock[6031]); // cannot complete
    // until thread 1 signals
Synchronization: The Dark Side

The naïve use of synchronization constraints can introduce its own set of problems, particularly when a thread requires access to more than one protected resource.

```c
void transfer(int account1, int account2, int amount) {
    wait(lock[account1]);
    wait(lock[account2]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
    signal(lock[account2]);
    signal(lock[account1]);
}
```

What can go wrong here?

Thread 1: wait(lock[6031]);
Thread 2: wait(lock[6004]);
Thread 1: wait(lock[6004]); // cannot complete
    // until thread 2 signals
Thread 2: wait(lock[6031]); // cannot complete
    // until thread 1 signals

No thread can make progress → Deadlock
Philosophers think deep thoughts, but have simple secular needs. When hungry, a group of $N$ philosophers will sit around a table with $N$ chopsticks interspersed between them. Food is served, and each philosopher enjoys a leisurely meal using the chopsticks on either side to eat.

They are exceedingly polite and patient, and each follows the following dining protocol:

**Philosopher’s algorithm:**
- Take (wait for) LEFT stick
- Take (wait for) RIGHT stick
- EAT until sated
- Replace both sticks
Philosophers think deep thoughts, but have simple secular needs. When hungry, a group of N philosophers will sit around a table with N chopsticks interspersed between them. Food is served, and each philosopher enjoys a leisurely meal using the chopsticks on either side to eat.

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Deadlock!

No one can make progress because they are all waiting for an unavailable resource.

**CONDITIONS:**

1) Mutual exclusion: Only one thread can hold a resource at a given time

2) Hold-and-wait: A thread holds allocated resources while waiting for others

3) No preemption: A resource can not be removed from a thread holding it

4) Circular wait
Deadlock!

No one can make progress because they are all waiting for an unavailable resource.

**CONDITIONS:**

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4) Circular wait

**SOLUTIONS:**

Avoidance
-or-
Detection and Recovery

He still doesn’t look too happy...

Cousin Tom is spared!
One Solution

Assign a unique number to each chopstick, request resources in a consistent order:

New Algorithm:
- Take LOW stick
- Take HIGH stick
- EAT
- Replace both sticks.
Assign a unique number to each chopstick, request resources in a consistent order:

**New Algorithm:**
- Take LOW stick
- Take HIGH stick
- EAT
- Replace both sticks.

**Simple proof:**
Deadlock means that each philosopher is waiting for a resource held by some other philosopher ...

But, the philosopher holding the highest numbered chopstick can’t be waiting for any other philosopher (no hold-and-wait cycle) ...

Thus, there can be no deadlock.
Can you fix the transfer method to avoid deadlock?

```c
void transfer(int account1, int account2, int amount) {
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
}
```

Example: Dealing With Deadlocks

ATM

May 8, 2018
Example: Dealing With Deadlocks

Can you fix the transfer method to avoid deadlock?

```c
void transfer(int account1, int account2, int amount) {
    int a = min(account1, account2);
    int b = max(account1, account2);

    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
}
```

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Can you fix the transfer method to avoid deadlock?

```c
void transfer(int account1, int account2, int amount) {
    int a = min(account1, account2);
    int b = max(account1, account2);
    wait(lock[a]);
    wait(lock[b]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
}
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Can you fix the transfer method to avoid deadlock?

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void transfer(int account1, int account2, int amount) {
    int a = min(account1, account2);
    int b = max(account1, account2);
    wait(lock[a]);
    wait(lock[b]);
    balance[account1] = balance[account1] - amount;
    balance[account2] = balance[account2] + amount;
    signal(lock[b]);
    signal(lock[a]);
}
```
Summary

• Communication among parallel threads or asynchronous processes requires synchronization
  – Precedence constraints: a partial ordering among operations
  – Semaphores as a mechanism for enforcing precedence constraints
  – Mutual exclusion (critical sections, atomic transactions) as a common compound precedence constraint
  – Solving Mutual Exclusion via binary semaphores
  – Synchronization serializes operations, limits parallel execution

• Many alternative synchronization mechanisms exist!

• Deadlock:
  – Consequence of undisciplined use of synchronization mechanism
  – Can be avoided in special cases, detected and corrected in others
Thank you!

Next lecture: 
*Putting it all together*