Problem 1. Binary Arithmetic (10 points)

(A) (4 points) What is \( \sim(0xC7) \oplus 0x1F \), where \( \sim \) is bitwise NOT and \( \oplus \) is bitwise XOR? Provide your result in both binary and hexadecimal. Show your work for partial credit.

Result in binary (0b): ___________________________
Result in hexadecimal (0x): ___________________________

(B) (3 points) Multiply 9 by 5 using unsigned binary numbers. Show all of your work by filling in the missing rows in the table below and provide your final answer in binary.

\[
\begin{array}{c|c c c | c c c}
1 & 0 & 0 & 1 & 1 & 1 & 0 \\
0 & 1 & 0 & 1 & 0 & 0 & 1 \\
\end{array}
\]

\( 9 \times 5 \) (0b): ___________________________

(C) (3 points) What is \(-41\) in 8-bit 2’s complement notation? Provide your answer in binary and hexadecimal.

-41 in binary (using 8-bit 2’s complement notation) (0b): ___________________________
-41 in hexadecimal (0x): ___________________________
Problem 2. RISC-V Assembly (18 points)

(A) You are given the following C code along with an incomplete translation of this code into RISC-V assembly. Assume that execution ends when it reaches the unimp instruction.

```c
int a[4] = {0x1, 0x2, 0x4, 0x8}; // a[0]=0x1, a[1]=0x2, etc.
int b = 0x5;
int c = 0;
for (int i = 3; i >= 0; i = i-1) {
    c = a[i] & b; // & is bitwise-AND
    if (c != 0){
        break;
    }
}
```

// Translation into RISC-V assembly:
```c
. = 0x0
li a0, 0x800  // starting address of array
li a1, 0x5
li a2, 0x3
loop: mv t0, a2

slli t0, t0, 2

add t0, a0, t0
lw t1, 0(t0)
and t1, t1, a1
bnez t1, end;
bnez t1, end;
bgez a2, a2, -1
bgez a2, loop
end: unimp

. = 0x800  // contents of array a, where a[0] is at 0x800
.word 0x1
.word 0x2
.word 0x4
.word 0x8
```

1) (4 Points) Complete the RISC-V translation by:
   a. Inserting the loop label where it belongs in the code.
   b. Filling in the blank box with the missing instruction.

2) (6 Points) Provide the values left in the registers below following the execution of the corrected code:

   Value left in t0: \(0x808\)  
   Value left in t1: \(0x4\)  
   Value left in a2: \(0x2\)
(B) (8 points) Provide the values left in the registers after executing the code below. Assume that execution ends when it reaches the `unimp` instruction. (. = 0x0 means that the first addi instruction is at address 0x0. Zero is x0 which = 0).

```
. = 0x0
    addi a1, zero, 0x234
    addi a3, zero, 0x1
    jal a2, L1
    xor a3, a1, a1
    beqz a3, end
    j L2

L1: addi a2, a2, 4
    jr a2
L2: add a3, a1, a1

end: unimp
```

Value left in a2: 0x10_______________
Value left in a3: 0x468_______________
Problem 3. RISC-V Assembly and Calling Convention (18 points)

(A) (8 points) The `getPower` function in C given below recursively computes the power of a number (e.g. `getPower(2,3) = 2^3 = 8`). Since our RISC-V processor does not have a multiply instruction, the code uses a multiply procedure, `mult`, which multiplies two signed integers. The implementation of the `mult` procedure is not provided to you, but it does obey the calling convention.

The RISC-V code given below is not behaving as expected and you are tasked with fixing the bugs so that the code works correctly and follows the RISC-V calling convention. You’re allowed to add up to 5 instructions to make it work. You are not allowed to modify or remove any of the given instructions, but feel free to change the order in which they appear.

```c
int getPower(int b, int p){
    if (p == 0)
        return 1;
    else
        return mult(getPower(b, p-1), b);
}
```

Provide the correct assembly code here. Your implementation should obey the RISC-V calling convention.

```risc-v
int getPower(int b, int p){
    if (p == 0)
        return 1;
    else
        return mult(getPower(b, p-1), b);
}
```

With the correct RISC-V code, what is the value of `sp` at the pointed instruction (beqz a1, end2) during its third recursive call to `getPower`? Assume that the original call to `getPower` is not considered one of the recursive calls. **Assume sp is 0x101C (in hex) initially.** Assume p > 3.

```risc-v
sp (0x) = ____________________0x1004____________________
```

getPower:

```risc-v
    beqz a1, end2
    addi sp, sp, -8
    sw a0, 0(sp)
    sw ra, 4(sp)
    addi a1, a1, -1
    jal getPower
    jal mult
end1:
    lw ra, 4(sp)
    addi sp, sp, 8
    ret
end2:
    li a0, 1
    ret
mult: ...
```
(B) (10 points) The `getSSD` function given below computes the sum square distance between the elements in arrays `a` and `b` (i.e. `getSSD(a[], b[], size) = \sum_{i=0}^{size-1} (a[i] - b[i])^2`). Again, this code uses a multiply procedure, `mult`, which multiplies two signed integers. The implementation of the `mult` procedure is not provided to you, but it does obey the calling convention. Please fix the bugs in the given RISC-V code such that it works correctly and follows the RISC-V calling convention. Assume arrays `a` and `b` have the same size. You’re allowed to add up to 13 instructions to make it work. Feel free to change the order of instructions, but you are NOT allowed to modify any of the given instructions.

```assembly
// a0 -> base address for a
// a1 -> base address for b
// a2 -> size
getSSD:
    mv s1, a0 // a address
    mv s2, a1 // b address
    slli a2, a2, 2
    add a2, a0, a2 // end address of a
    li s0, 0 // s0 holds running sum
    j compare
loop:
    lw a4, 0(s1)
    lw a5, 0(s2)
    sub a0, a4, a5
    jal mult
    add s0, s0, a0
    addi s1, s1, 4
    addi s2, s2, 4
compare:
    bgt a2, s1, loop
end:
    mv a0, s0
    ret
```

Provide the correct assembly code here. Your implementation should obey the RISC-V calling convention. Feel free to only show the changes to the code with arrows to where the changes go in the original code.

getSSD:
    addi sp, sp, -20
    sw ra, 0(sp)
    sw s0, 4(sp)
    sw s1, 8(sp)
    sw s2, 12(sp)
    mv s1, a0
    mv s2, a1
    slli a2, a2, 2
    add a2, a0, a2 // get the end address
    li s0, 0 // s0 holds running sum, return 0 if the size <= 0
    j compare
loop:
    lw a4, 0(s1)
    lw a5, 0(s2)
    sub a0, a4, a5
    mv a1, a0
    sw a2, 16(sp)
    jal mult
    lw a2, 16(sp)
    add s0, s0, a0
    addi s1, s1, 4
    addi s2, s2, 4
compare:
    bgt a2, s1, loop
end:
    mv a0, s0
    lw ra, 0(sp)
    lw s0, 4(sp)
    lw s1, 8(sp)
    lw s2, 12(sp)
    addi sp, sp, 20
    ret
Problem 4. Stack Detective (17 points)

Consider the C procedure below and its translation to RISC-V assembly code, shown on the right.

```c
int f(int a, int b) {
    int c = b - a;
    if (c & ??? == 0) // c is a multiple of 4
        return 1;
    else {
        int d = f(a - 1, b + 2);
        return 3 * (d + a);
    }
}
```

(A) (2 points) What value should the ??? term in the C code and the assembly be replaced with to make the if statement correctly check if the variable ‘c’ is a multiple of 4? (Hint: In the collatz program in Lab 1 we learned that we can test if a number is even, or a multiple of 2, by checking if its last bit is 0.)

Correct value of ‘???’ term? _____3_____

(B) (2 points) How many words will be written to the stack before the program makes each recursive call to the function f?

Number of words pushed onto stack before each recursive call? _____2_____
The program’s initial call to function `f` occurs outside of the function definition via the instruction `jal ra, f`. The program is interrupted at an execution (not necessarily the first) of function `f`, just prior to the execution of `add a0, a0, a1` at label `L1`. The diagram on the right shows the contents of a region of memory. All addresses and data values are shown in hex. The current value in the SP register is 0xEB0 and points to the location shown in the diagram.

(C) (4 points) What were the values of arguments `a` and `b` to the initial call to `f`? Write CAN’T TELL if the argument does not show up in the stack.

`a` is passed in `a0`, can find saved `a0` from initial call right before external return address. `b` is never saved, so can’t tell

Initial arguments to `f`: `a = ____4____ ; b = ___CAN’T TELL___`

(D) (4 points) What are the values in the following registers right when the execution of `f` is interrupted? Write CAN’T TELL if you cannot tell.

These registers were just loaded from the stack at the time of interruption

<table>
<thead>
<tr>
<th>Address</th>
<th>Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xEA4</td>
<td>0x0</td>
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<tr>
<td>0xEA8</td>
<td>0x1</td>
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<tr>
<td>0xEA0</td>
<td>0xA4</td>
</tr>
<tr>
<td>0xEB0</td>
<td>0x2</td>
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<td>0xEB4</td>
<td>0xA4</td>
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<tr>
<td>0xEB8</td>
<td>0x3</td>
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<td>0xEBC</td>
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<td>0xEC0</td>
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<td>0xEC0</td>
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<td>0xED0</td>
<td>0xCE8</td>
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</tbody>
</table>

Saved `a0` | Saved `ra` | Saved `a0` | Saved `ra` | Saved `a0` | Saved `ra`

(Saved `ra` of initial call)

(E) (2 points) What is the hex address of the `jal ra, f` instruction that made the initial call to `f`?

Saved `ra` of initial call is `0x3B8`, call occurs `0x4` before that at `0x3B4`

Address of instruction that made initial call to `f`: `0x ___3B4___`

(F) (3 points) What is the hex address of the instruction at label ELSE?

Ra saved pointing to `lw a1, 0(sp)` is at `0xA4`, ELSE is `0x18` before at `0x8C`

Address of instruction at label ELSE: `0x ____8C____`
Problem 5. Boolean Algebra and Combinational Logic (21 points)

You are given the truth table for a circuit that takes a 3-bit unsigned binary input (X = ABC), adds 3 mod 8 to it to produce a 3-bit unsigned binary output (Y = A'B'C').

<table>
<thead>
<tr>
<th>A</th>
<th>B</th>
<th>C</th>
<th>A'</th>
<th>B'</th>
<th>C'</th>
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</tbody>
</table>

(A) (9 points) For the above truth table, write out a **minimal sum-of-products** for each function A'(A,B,C), B'(A,B,C), and C'(A,B,C)

Minimal sum-of-products for A'(A,B,C) = \( (~A)C + (~A)B + A(~B)(~C) \)

Minimal sum-of-products for B'(A,B,C) = \( (~B)(~C) + BC \)

Minimal sum-of-products for C'(A,B,C) = \( ~C \)
(B) (3 points) Which one of these functions can be used to build any boolean function (i.e., it’s a universal gate)? Assume that you may tie inputs to 1 or 0 if necessary. Explain your answer.

\[ A' \quad B' \quad C' \]

\[ A'(1, B, C) = (~B)(~C) = ~(B + C) = \text{NOR gate which is universal} \]

(C) (3 points) What function, of A and B, does the following circuit implement?

\[ Y(A, B) = \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ A + B \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \]

(D) (4 points) Draw the circuit for \( B' \) using only 2-input 1-bit muxes. Make sure to label all the inputs and the output of your circuit.
(E) (2 points) You are told that the propagation delay of a 2-input 1-bit mux is 2ns. What is the propagation delay for output B’ based on the circuit you drew above?

Propagation Delay of B’ (ns) = __________ 4 or 6 ______________